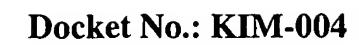


**PATENT** 





## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Masahiko YOSHIMOTO et al.

U.S. Patent Application No. 10/553,651

Filed: January 17, 2006 : Examiner:

For: MOVING PICTURE ENCODING OR DECODING PROCESSING SYSTEM AND MOVING PICTURE ENCODING OR DECODING PROCESSING METHOD

Group Art Unit: 2611

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## SUBMISSION OF INFORMATION DISCLOSURE STATEMENT

Sir:

Submitted herewith are Information Disclosure Statement, Japanese Action and five references.

It is certified that the Japanese Action and the references were issued on April 28, 2008 and each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

In the Action, claims 1-5 and 10-14 were rejected by the references 1-3, claims 6 and 15 were rejected by the references 1-3, claims 7 and 16 were rejected by the references 1-4, claims 8 and 17 were rejected by the references 1-4, and claims 9 and 18 were rejected by the references 1-5.

- 1. Japanese Publication No. H09-009251
- 2. Hiroshi Kawaguchi, et al., An LSI for V<sub>DD</sub>-Hopping and MPEG4 System Based on the Chip, The 2001 IEEE International Symposium on Circuits and Systems, 2001. ISCAS 2001, IEEE, May 6, 2001, vol. 4, p.918-921
- 3. Fuminori Osako, and the other four inventors., A Dynamic Computation Resource Scalable Algorithm for Software Video CODEC, Journal of Institute of Electronics, Information and Communication Engineers D-II, Institute of Electronics, Information and Communication Engineers, February 25, 1997, No. J80-D-II, No. 2, p.444-458
- 4. Japanese Publication No.2002-010271
- 5. Japanese Publication No. H10-248063

## Respectfully submitted,

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